- **Untar ATF atf-${date code}-${commit ID}.tar.xz and compile it**
- **For example :**
  - CMD:~/$> tar -Jxvf atf-20220606-637ba581b.tar.xz
  - CMD:~/$> cd atf-20220606-637ba581b/
  - CMD:~/ atf-20220606-637ba581b$> make menuconfig
    - Select "MT7981"

```
Platform  --->
Target  --->
DRAM  --->
```

```
Target Platforms (MT7981)  --->
[ ] Build for FPGA emulation (NEW)
```

    - Select "BL2 + FIP image without secure boot" for Target Image, "spim-snand" and enable NMBM. Also modify nand flash type according to your Flash type.

```
Platform  --->
Target  --->
DRAM  --->
Log  --->
```

```
Target Images (BL2 + FIP image without secure boot)  --->
[ ] Enable Secure Boot (NEW)
Target Flash Devices (spim-snand)  --->
[*] Enable NAND mapping block management (NEW)
(spim:2k+64) nand flash type (NEW)
```

    - Select "DDR3" or "DDR4" and "Auto detect"

```
Platform  --->
Target  --->
DRAM  --->
```

```
DRAM type (DDR3)  --->
DRAM size (Auto detect)  --->
```

```
(Top) → DRAM

DRAM type (DDR3)  --->
BOARD type (BGA)  --->
DRAM size limitation (Auto detect)  --->
DDR3 frequency (DDR3 2133MHz)  --->
```

If it's 7981a or 7981b > BGA
If it's 7981c > FCN

    - Enter u-boot.bin image path

```
Platform  --->
Target  --->
DRAM  --->
Log  --->
(../Uboot-upstream/u-boot.bin) Path to BL33 image
(/usr/bin/aarch64-linux-gnu-) cross compile prefix
```

  - CMD:~/ atf-20220606-637ba581b$> make
- **BL2 image is locatd in atf-${date code}-${commit ID}/build/mt7981/release/bl2.img**
- **FIP image is locatd in atf-${date code}-${commit ID}/build/mt7981/release/fip.bin**

Note: It's bl2.img

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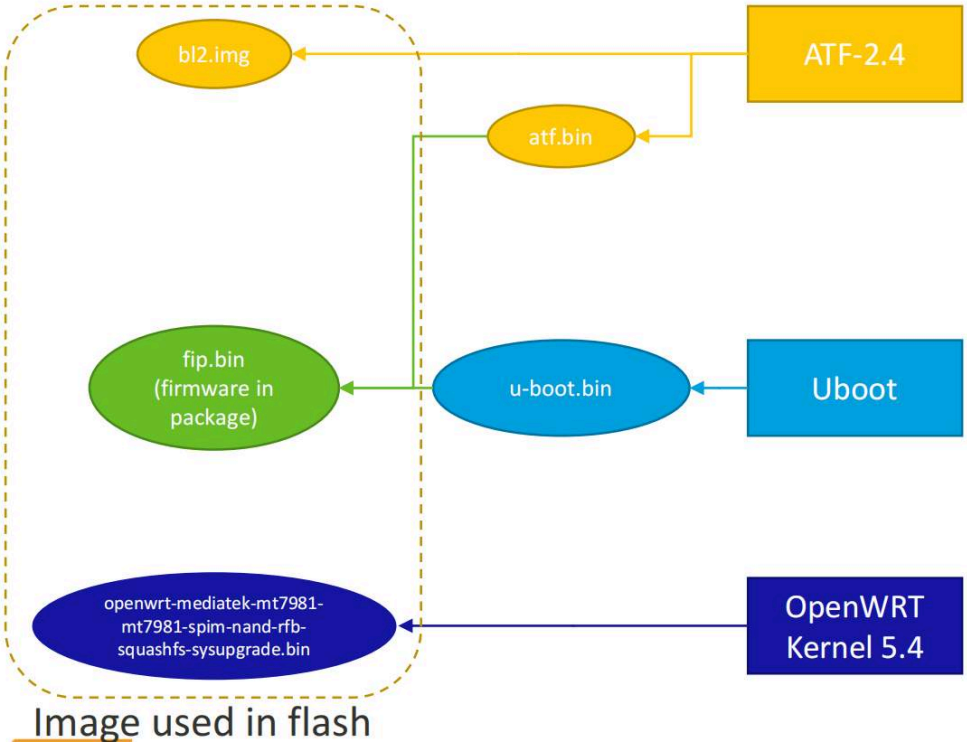
Cheetah Bootloader and SDK Image Layout



- **ATF-2.4**
  - **Cheetah's preloader is integrated in ATF. ATF will build atf-bl2, which is Cheetah's preloader.**
  - **ATF also build atf.bin which is ATF-BL31**
- **Uboot**
  - **Uboot build u-boot.bin, and atf will combind uboot image and atf-bl31 together as fip.bin**
- **OpenWRT**
  - **With Linux Kernel-5.4**

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